

SAMSUNG PROPRIETARY

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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

Power Sequence Timing



PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
USB Hub to PCI	AD29(internal)] Programmable	
LPC Bridge/IDE/AC97/SMBUS	AD30(internal)		
Internal MAC	AD31(internal)		
	AD24(internal)		

I²C / SMB Address

Devices	Address	Hex	Bus
TigerPoint	Master		SMBUS Master
OK-505M (Clock Generator)	1101 001X	D2h	Clock, Unused Clock Output Disable
SODIMMO	1010 000X	A0h	-
CPU Thermal Sensor	0111 101X	7Ah	Thermal Sensor

USB PORT Assign

Port Number	ASSIGNED TO	Port Number	ASSIGNED TO
UHCL_0	0 USB Port_P0	UHCL_2	4 MMC
	1 NC	UHCL_3	5 Mini Card 2(For HSDPA)
UHCL_1	2 USB Port_P2		6 NC
	3 Mini Card 1(For WLAN)		7 CAMERA

PCI Express Assign

Port Number	ASSIGNED TO
1	Mini Card 1(Wireless LAN)
2	NC
3	LOM
4	NC

Voltage Rails

Power Rail	Descriptions
PRTC_BAT	3.3V (can drop to 2.0V min. in G3 state) supply for the RTC well.
VDC	Primary DC system power supply (9 to 19V)
P1.05V(VCCP)	VTT for CPU, Calistoga & ICH7-M
P3.3V_MICOP	3.3V always power rail(for Microm)
P1.5V	1.5V switched power rail (off in S3-S5)
P1.5V_AUX	1.5V power rail for DDR (off in S4-S5)
P0.75V	0.75V power rail for DDR (off in S4-S5)
P5V_AUX	5.0V power rail (off in S4-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P5V	5.0V switched power rail (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
CPU_CORE	Core voltage for Atom CPU
P1.8V	For display logic of CPU

DRAW	Qin wenchao	DATE	12/24/2010	TITLE	Eric-VE-R INFORMATION Board Information	SAMSUNG ELECTRONICS
CHECK	Wu shijiang	DEV. STEP	PR			
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