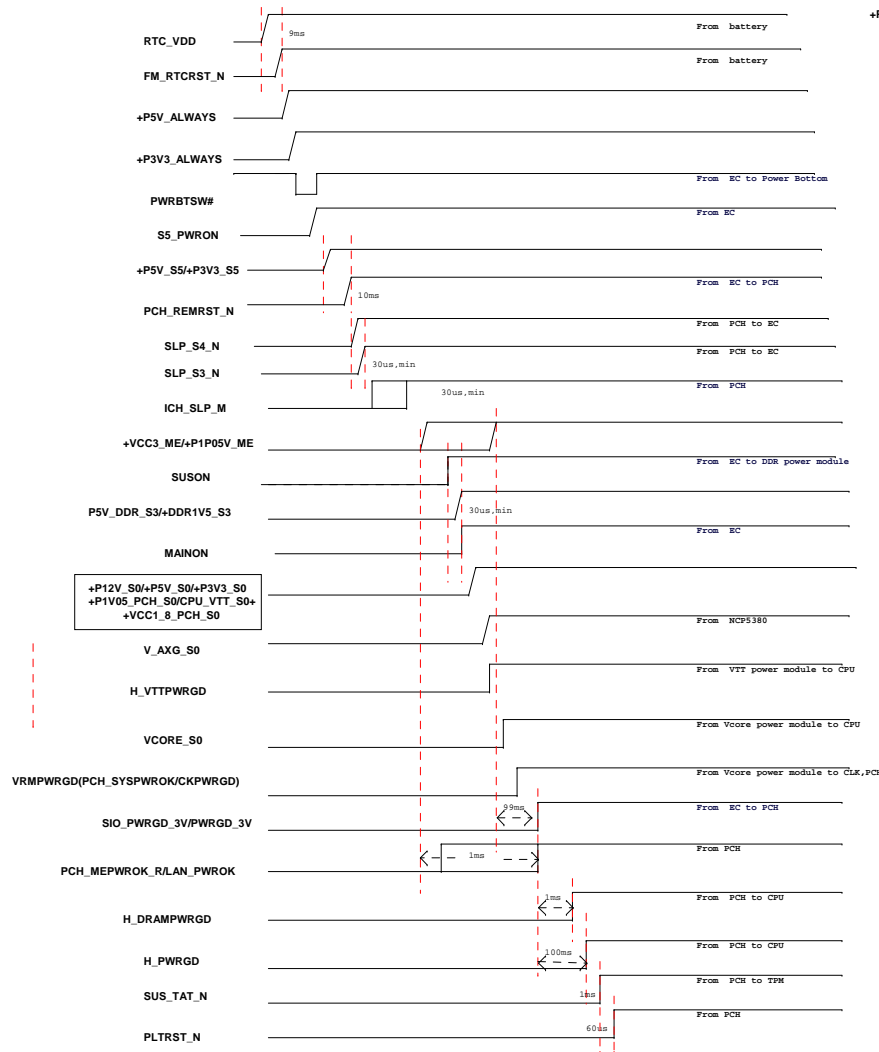
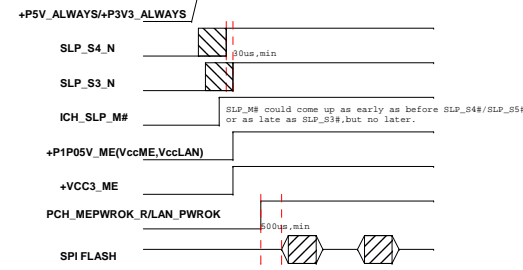


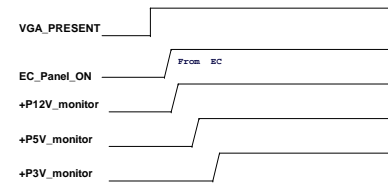
power on Sequence



SLP_M#, M3 ON



VGA_IN



Voltage Rails

Power	Voltage	S0-S2	S3	S4	S5	Ctl Signal
+P5V_ALWAYS	5V	V	V	V	V	
+P3V3_ALWAYS	3.3V	V	V	V	V	
+P5V_S5	5V	V	V	V	V	S5_PWRON
+P3V3_S5	3.3V	V	V			S5_PWRON
+VCC3_ME	3V	V	V			ME_G
+P1P05V_ME	1.05V	V	V			ICH_SLP_M
P5V_DDR_S3	5V	V				BUS0/MAINON
+DDR1V5_S3	1.5V	V				BUS0/MAINON
+P12V_S0	12V	V				MAINON
+P5V_S0	5V	V				MAINON
+P3V3_S0	3.3V	V				MAINON
+VCC1_8_PCH_S0	1.8V	V				MAINON
P15V	15V	V				MAINON
+P1V05_PCH_S0	1.05V	V				MAINON
CPU_VTT_S0+	1.1V	V				MAINON
V_AXG_S0	1.1	V				CPU_VTT_S0+
VCORE_S0	1.1	V				H_VTTPWRGD
+P12V_monitor	12V	V				EC_Panel_ON
+P5V_monitor	5V	V				EC_Panel_ON
+P3V_monitor	3V	V				EC_Panel_ON