

## Discrete Power On Sequence [AC in] [Battery only, AC absent]

ITEM	Measure Point			Time	ITEM	Measure Point			Time
Та	+DC_IN	То	+3V_VC						
Tb	+DC_IN	То	ACAV_IN						
Тс	+DC_IN	То	B+						
Td	ACAV_IN	То	ALWON						
Te	ALWON	То	+3VALW		Te	ALWON	To	+3VALW	
Tf	ALWON	То	+5VALW		Tf	ALWON	To	+5VALW	
Tg	+3VALW	То	ALW_PWRGD_3V_5V		Tg	+3VALW	To	ALW_PWRGD_3V_5V	
Th	ALWON	То	PCH_DPWROK		Th	ALWON	To	PCH_DPWROK	
Ti	B+	To	+3VLP		Ti	B+	To	+3VLP	
Tj	POWER_SW_IN#	То	POWER_SW_IN#		Tj	POWER_SW_IN#	To	POWER_SW_IN#	
					Tk	POWER_SW_IN#	To	ALWON	
					Tm	B+	To	+3V_VC	

ITEM	ITEM Measure Point Time							
T1	+3VALW	То	SIO_SLP_SUS#					
T2	SIO_SLP_SUS#	То	+3V_PCH					
T3	SIO_SLP_SUS#	То	+1.8VA					
T4	SIO_SLP_SUS#	То	+1.0V_PRIM_CORE					
T5	SIO_SLP_SUS#	То	+1.0VA					
T6	+3VALW	То	MPHYP_PWR_EN					
T7	MPHYP_PWR_EN	То	+1.0V_MPHYGT					
T8	SIO_SLP_SUS#	То	SUSACK#					
T9	+1.0VA	То	PCH_RSMRST#					
T10	PCH_RSMRST#	То	AC_PRESENT					
T11	PCH_RSMRST#	То	SIO_SLP_S5#					
T12	SIO_SLP_S5#	То	SIO_SLP_A#					
T13	SIO_SLP_S5#	То	SIO_SLP_WLAN#					
T14	SIO_SLP_WLAN#	То	AUX_EN_WOWL					
T15	AUX_EN_WOWL	То	=3VS_NGFF					
T16	SIO_SLP_S5#	То	SIO_SLP_S4#					
T17	SIO_SLP_S4#	То	+1.0V_VCCST					
T18	SIO_SLP_S4#	То	SUS_ON_EC					
T19	SUS_ON_EC	То	+1.8VU					
T20	SUS_ON_EC	То	+1.2V_DDR					
T21	SIO_SLP_S4#	То	VCCST_PWRGD					
T22	SIO_SLP_S4#	То	SIO_SLP_S3#					
T23	SIO SLP S3#	То	RUN ON EC					
T24	RUN_ON_EC	То	+3.3VDX_SSD					
T25	RUN_ON_EC	То	+1.0VS_VCCSTG					
T26	RUN_ON_EC	То	+1.0VS_VCCIO					
T27	RUN_ON_EC	То	+3VS					
T28	RUN_ON_EC	То	+5VS					
T29	+3VS	То	RUNPWROK					
T30	+3VS	То	MVP_VR_ON					
T31	IMVP_VR_ON	То	VCORE_PG (PCH_PWROK)					
T32	VCORE_PG (PCH_PWROK)	То	+VCC_SA					
T33	IMVP_VR_ON	То	+VCC_EDRAM					
T34	IMVP_VR_ON	То	+VCC_EOPIO					
T35	IMVP_VR_ON	То	SYS_PWROK (RESET_OUT#)					
T36	PCH_PLTRST#	To	+VCC_CORE					
T37	PCH PLTRST#	To	LVCC GT					

Security Classification	Corr	pal Secret Data	Compal Electronics, Inc.	
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